

APPLICATION  
FOR  
UNITED STATES LETTERS PATENT

TITLE: DYNAMIC EQUALIZING

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TITLE

DYNAMIC EQUALIZING

The present invention relates in general to dynamic equalizing, and more particularly concerns dynamic equalizing incorporating level sensing and manually selected volume sensing.

BACKGROUND OF THE INVENTION

For background, reference is made to U.S. Patent Nos. RE37,223 and 5,361,381. It is an  
5 important object of the invention to provide improved dynamic equalizing.

COMPUTER PROGRAM LISTING APPENDIX

The material on the compact disc DYNAMIC EQUALIZING created January 14, 2004, containing 6K bytes is incorporated by reference.

SUMMARY OF THE INVENTION

10 Frequency response is adjusted dynamically in response to level sensing of an input signal and the setting of the manually set volume control, where the level sensing occurs before the signal is delivered to the manually set volume control.

15 According to an aspect of the invention, time constants for frequency response changes are established for reducing compressor artifacts. Another feature for reducing compressor artifacts includes limiting the minimum value of the detected input.

Other features, objects and advantages of the invention will become apparent from the following detailed description when read in connection with the accompanying drawing in which:

BRIEF DESCRIPTION OF THE SEVERAL VIEWS OF THE DRAWING

20 FIG. 1 shows the logical arrangement of a system according to the invention; and  
FIG. 2 is a block diagram of portions of a radio embodying the invention.

DETAILED DESCRIPTION

With reference now to the drawing and more particularly FIG. 1, there is shown a block diagram illustrating the logical arrangement of a system according to the invention. The

invention may be embodied in hardware or a combination of hardware and software, and may be accomplished using analog circuits, digital signal processing techniques or a combination. A specific embodiment has a flash memory storing program instructions for a digital signal processor chip.

5       The system processes an input audio signal on input terminal 11 to provide an output signal dynamically equalized according to the invention on output terminal 12. The input signal on terminal 11 is delivered to manually controlled volume control 13 whose gain is set by a manually set volume setting signal on line 14 that is also delivered to adder 18. The input signal on terminal 11 is also delivered to level detector 15. Level detector 15 is typically a peak 10 detector, although other level detectors may also be used. Level detector 15 provides a linear signal representative of the level of the input signal, which is then logarithmically processed 16 to furnish a signal to Max 17 that is representative of the input signal level in decibels (db). In one embodiment, the detected level for a full scale input signal is scaled to be 0 dB, Constant 19, which has a value of 20 dB, is used to set the range of allowable output values for Max 17, which 15 is between 0 dB and -20 dB in this example. However, other values can be chosen for constant 18. Max 17, comprising a limiter, feeds its output back to level detector 15 to limit the minimum level to which the output of level detector 15 is allowed to decay. For example, if the level of input signal 11 drops below -20 dB re full scale, feedback signal 21 from Max 17 does not allow the output of level detector 15 to drop below -20 dB. Adder 18 delivers a first sum signal on line 20 23 to a second adder 24 that receives a system calibration constant on line 25. Adders 18 and 24 could also be combined into a single addition operation if desired. The system calibration constant added to the output of adder 18 provides a calibrated sum signal on output line 26 that is delivered to minimum level controller 27.

25      The SysCal constant is representative of the specific system in which the invention is used. SysCal is the number that when added to the value present at the output of adder 18 gives an estimate representative of the SPL obtained (available on line 26) when the associated sound system in which the invention is incorporated is operating in a typical room (assuming that the amplifier is functioning in its linear range). It compensates for any gain present in the system between the output of adder 18 and the actual SPL present, including amplifier gain, transducer 30 gain (from electrical input to SPL output), and room gain. One method of determining SysCal for a particular embodiment involves operating a system employing the invention in a

representative room. The output of adder 18, and the sound pressure present in the room (measured in dB SPL) are measured simultaneously. The difference between these values is the SysCal value. For a specific radio embodying the invention the SysCal constant is 114 db.

Minimum level controller 27 limits the maximum estimate of the output sound pressure level to a level set by the Max SPL constant on line 31, about 90 db for the aforesaid specific radio. The Max SPL constant is chosen to approximately match the maximum SPL that the electroacoustic system can produce in a typical room, and accounts for the large signal behavior of the system. Minimum level controller 27 keeps the estimate of sound pressure level from significantly exceeding the actual SPL present in the environment in which a system employing the invention is used, when the system operates at or near its maximum output capability. Over estimation of the SPL present in the environment would result in too little dynamic equalization being applied to the system. The combination of Max SPL constant and Minimum level controller 27 are used to compensate for the fact that under large signal conditions, the system gain (primarily the electrical gain but may also include the gain of the acoustic system) decreases. At some point as the input signal level increases, the output sound pressure level will no longer increase.

A typical system may include a system limiter that can be used to keep the system amplifier from clipping. The limiter achieves this by dynamically reducing system (electrical) gain when a signal is presented to the amplifier input that would be large enough to cause the amplifier to clip. Rather than using a Max SPL constant as described, the SPL estimate could be limited by a modified value that dynamically tracked the system gain. An output from a system limiter could be fed back to Minimum level controller 27 to keep the SPL estimate from exceeding the actual SPL present in the environment.

Minimum level controller 27 provides an output signal on line 32 that is an estimate of sound pressure level encountered by a listener listening to an audio system with electroacoustic devices (amplifiers and loudspeakers, not shown) driven by output signal 12. The SPL estimate signal is delivered to loudness mapping function 33 via line 32. Mapping function 33 determines the relationship between the SPL estimate and the gain signal provided on line 34 to gain controller 35. Mapping function 33 is typically configured as a lookup table, but could also be calculated from a function generated to describe the desired mapping behavior. The form of the mapping function depends on the topology of the elements used to dynamically equalize the

desired signal. Derivation of a representative mapping function for an embodiment employing the topology shown in Fig. 1 is described below. The mapping function describes a relationship between low frequency equalization and sound pressure level. The relationship is independent of the system in which it is used, except for topology. It is also possible to construct a mapping function that is completely independent of the system, including topology. In this case, a separate block would be needed to translate the mapping function for use with a particular topology.

Use in different systems of the dynamic EQ described herein would typically require modification of SysCal and Max SPL constants, but not require change to the mapping function. It should be noted that it is also possible to incorporate SysCal and Max SPL functions into a single mapping function, if desired. Such an arrangement would work identically to the system of Fig. 1, except that the mapping function would no longer be independent of the system in which it was used, which complicates the manufacture of multiple devices. The structure of Fig. 1 separates system dependent and system independent functions out for improved portability of the invention across products.

Gain controller 35 controls the level of the output signal provided by a filter of bass spectral components, such as band pass filter 36, which typically has a center frequency at the lowest frequency radiated by the system and is energized by the output of manually controlled volume control 13, that is provided to output adder 37. Output adder 37 combines the manually controlled input signal with the signal provided by gain controller 35 to provide the output signal on output 12 that is dynamically equalized according to the invention. In effect, the resultant signal has spectral components between about 200 Hz and the center frequency of band pass filter 36 that are progressively amplified as a function of frequency that increases as frequency decreases by an amount related to both the sensed input level and the volume control setting, as determined by mapping function or lookup table 33.

Mapping function 33 for an embodiment employing the topology of Fig. 1 can be derived from the data graphically represented in FIG. 6 of the aforesaid U.S. Pat. No. RE37,223. The top curve of Fig. 6 is associated with a level of 94 dB SPL. The center frequency of bandpass filter 36 is associated with the low frequency peak of the family of curves, in this embodiment approximately 50 Hz. Derivation of a mapping parameter for the curve in Fig. 6 marked 65% will be illustrated. The 65% curve corresponds to an SPL of approximately 71 dB SPL. The

curve (looking at high frequencies where no bass boost is active) is approximately 23 dB lower than the top curve, which is referenced to 94 dBSPL. The required gain of gain block 36, for an estimated SPL of 71 dBSPL, is determined by comparing the magnitude of the 65% curve at the low frequency peak (50 Hz) to the magnitude at high frequencies. For the 65% curve, the high frequency level is approximately -10.5 dB and the level of the peak is approximately -2 dB. Therefore, the gain should be approximately 8.5 dB. Values for other estimated SPL levels can be determined in a similar manner, and the resulting values entered into a lookup table. Values for SPL estimates that fall between the curves shown in Fig. 6 can be interpolated. Alternatively, a polynomial or other function could be fit to the series of values obtained, and the function calculated whenever a gain value is needed.

The present invention has a number of advantages. By level sensing prior to delivering the signal to the manually set volume control, the advantage of volume control setting responsiveness is obtained. The responsiveness of dynamic equalizing to volume control changes and signal level changes may be set independently. It is preferable that dynamic equalizing that compensates for changes in volume control setting occur instantaneously (although time constants can be associated with these changes if desired) whereas dynamic equalizing that compensates for changes in input signal level have time constants applied to reduce audible artifacts (time constants are discussed in more detail below). This arrangement avoids momentary loss of bass that may occur for some length of time in a level sensing dynamic equalization system when the input signal level is reduced as a result of manual reduction of system volume. Embodiments of the present invention allow different time constants to be used for equalization adjustment associated with manual volume level adjustments and signal level variations.

In one embodiment, the side chain processing (the side chain consisting of elements 15 – 25 19, 21, 23-27, 31, 32, 33) is done in blocks. 256 samples (approximately 5.8 msec of audio data) are acquired and processed. Level detector 15 calculates the RMS value of the samples in a block, dB 16 converts this calculated value into a logarithmic value, and Max 17 limits the range of variation of these block values to 20 dB, and provides feedback to level detector 15 as previously discussed. The block size chosen fundamentally determines how quickly the level detector can change when the input level changes. The attack time constant is therefore related to the block size chosen, and in this example is approximately 5.8 msec. The decay time

constant is chosen to reduce audible artifacts associated with dynamically changing the equalization applied. In one embodiment, the decay time constant is chosen to be on the order of 10 seconds, although longer time constants, such as about 20 seconds, may be desirable.

5 An exemplary code for input level sensing follows:

```
k = p->dyneq.timeConstant; // current value = 0.99942 floating point = exp(-1/1723), about 7  
frames = 256*7)  
  
p->dyneq.slower_smoothed_rms = MAX(m, scalarMult(k, p->dyneq.slower_smoothed_rms));
```

In other words, this approach is a fast attack and slow decay approach. Every frame, 256  
10 samples (or about 5.8 msec) of data are acquired. The mean square signal (m) of the frame (or  
block) of 256 data samples is measured. If that (m) is bigger than the slowly decaying last  
estimate (called p.slower\_smoothed\_rms) then it (m) immediately becomes the new estimate,  
otherwise the old estimate is decayed with a time constant of 10 seconds.

Since the side chain processing is done on a block basis, the output of the mapping  
15 function 33 changes approximately once every 5.8 msec, which causes gain 35 to change once  
every 5.8 msec. A one pole low pass filter having a cutoff of 40 Hz is placed between the output  
of the mapping function and gain element 35 to smooth the gain changes to reduce audible  
artifacts (such as staircase or zipper noise) that might otherwise be perceptible if the gain 35 were  
20 to change in a stepwise fashion. Furthermore, time constants associated with signal level  
variations can be selected to reduce artifacts associated with time varying gains. Still a further  
advantage resides in reducing artifacts by limiting the minimum value of the detected input.  
Since the level detector need not accommodate the full dynamic range of the volume control, its  
range of values can be limited to the expected variation in source signal levels, typically of the  
order of 20 dB but may be smaller, compared with 60 db or more for post-volume detection.  
25 Furthermore, limiting the dynamic range of the level detector has the advantage of reducing the  
maximum error possible during a transient event, such as excess bass during a sudden attack  
which follows a quiet passage.

The invention typically forms an estimate of the sound pressure level (SPL) in the room  
by first detecting the input signal and converting it from a linear range to a logarithmic range in  
30 decibels (db). This detected level is limited to a range of values wide enough to accommodate  
the expected input sources. The volume setting is then added to the detected level to find the

effective electrical input level to the dynamic equalizer. A scale factor (SysCal) is then added to form an estimate of the SPL in the room. This estimate is then bounded to an upper limit to account for limitations of the playback system. The final SPL estimate is then used as an input to the desired loudness mapping function which creates the necessary band pass filter gain.

Referring to FIG. 2, there is shown a block diagram illustrating the logical arrangement of a radio portion embodying the invention. An audio input signal selected by switch 41 and delivered to the input of input analog-to-digital converter 42 is reproduced by loudspeaker 43 dynamically equalized according to the invention. The audio input signal, which may be an FM signal on terminal 41A, a CD signal on terminal 41B or an auxiliary signal, such as from a television, on terminal 41C is delivered to the input of analog-to-digital converter 42 to provide a corresponding digital signal that is delivered to digital signal processor 44 that receives a volume control setting signal from volume control 45 and exchanges digital information with flash memory 46 that has stored therein the program instructions referred to above and on the appended CD-ROM identified above. Digital signal processor 44 provides a dynamically equalized digital signal processed in the manner described above to digital-to-analog converter 47 that provides a corresponding dynamically equalized signal to the input of power amplifier 51 that energizes loudspeaker 42.

There has been described novel apparatus and techniques for dynamic equalizing. It is evident that those skilled in the art may now make numerous uses and modifications of and departures from the specific apparatus and techniques described herein without departing from the inventive concepts. Consequently, the invention is to be construed as embracing each and every novel feature and novel combination of features present in or possessed by the apparatus and techniques herein disclosed and limited solely by the spirit and scope of the appended claims.

What is claimed is: